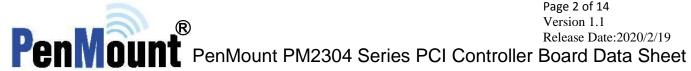
# PENMOUNT PM2304 CONTROL BOARD DATASHEET

Version 1.1 19/Feb/'20





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# **Revision history**

Rev.	Date	Ву	Summary	Remark
1.0	2019/11/11	Kenhsu	New Release	
1.1	2020/2/19	Kenhsu	Added I <sup>2</sup> C and Uart interfaces	

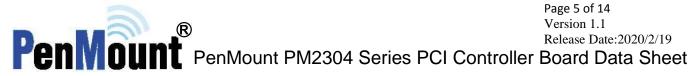
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### 1.0 Introduction

The PenMount PM2304 series control board is a high specification (Projected Capacitive Input, PCI) touch panel controller product introduced by PenMount. The PenMount PM2304 series can be applied in the consumer, commercial and the industrial fields.

The PenMount PM2304 series provides USB types of interfaces, USB \ I^2C \ UART and supports PCI touch panels sized from 8"~10.4". PenMount PM2304 series also supports a wide range of operating systems such as Windows and Linux.

The PenMount PM2304 series is developed based on Microchip microprocessors and is paired with PenMount's in-house hardware design and firmware algorithmic mechanism. It provides high performance computing and possesses excellent anti-noise capabilities.

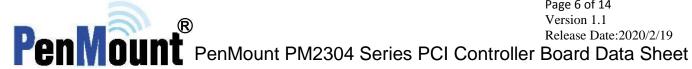


### **Specifications** 2.0

Parameter		feature		
Controller part r	number	PenMount P2-08		
Number of sens	ing line	23		
Number of drivi	ng line	40		
Supporting proje	ected capacitive	Projected capacitive type, from 8" to 10.4"		
touch panel size				
Interface	PM2205	USB Full-speed, 12Mbps		
		I <sup>2</sup> C Slave, support 100 / 400 kHz specifications		
		UART 38400 baud rate / 8bit data / non parity / one stop bit /		
		non-PnP		
	PM2205U	USB Full-speed, 12Mbps		
ADC resolution		10bits (Typical)		
Firmware resolu	tion	USB: 4096 x 4096 (Typical)		
		I <sup>2</sup> C / UART : 2048 x 2048 (Typical)		
Response time		Average < 40ms		
Sampling rate	One point	150 Hz(Typical)		
	Five points	100 Hz(Typical)		
Operating voltag	ge	+5Vdc ±5 %		
Power	Working mode	45mA @ 5Vdc (Typical)		
consumption	Idle mode	30mA @ 5Vdc (Typical)		
	Sleep mode	2mA @ 5Vdc (Typical)		
Operating temp	erature	-40°C ~ +85°C		
Storage temperature		-40°C ~ +85°C		
Relative humidity range		95% RH at 60°C. RH Non-condensing		
EMS RS		IEC61000-4-3 Level 3 , Criteria A		
specification CS		IEC61000-4-6 Level 3 , Criteria A		
Watchdog Time	r	Support WDT function through firmware programming		

# Note:

CS and RS performance, Power consumption and sample rate will vary according to different firmware versions.



### Mechanical drawing 3.0

# 3.1 Mechanical size



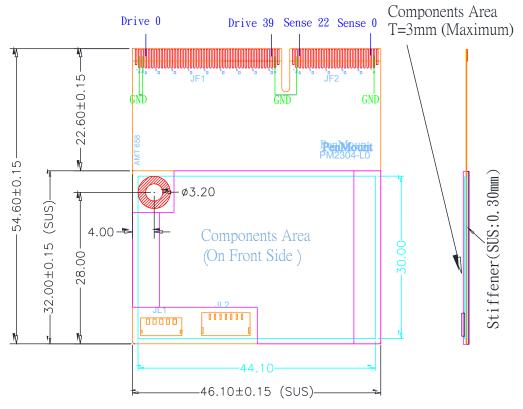
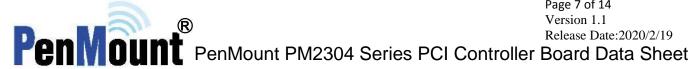


Figure 1 PM2304





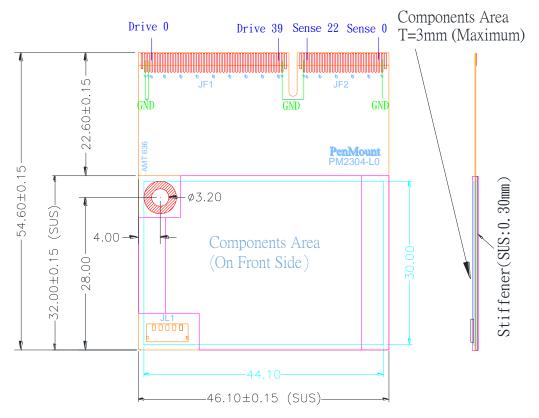


Figure 2 PM2304U

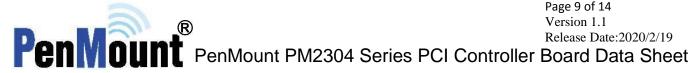


# 3.2 Touch line pin definition

JF2							
PIN	Description	PIN	Description	PIN	Description	PIN	Description
1	GND	8	Cap Sense 16	15	Cap Sense 9	22	Cap Sense 2
2	Cap Sense 22	9	Cap Sense 15	16	Cap Sense 8	23	Cap Sense 1
3	Cap Sense 21	10	Cap Sense 14	17	Cap Sense 7	24	Cap Sense 0
4	Cap Sense 20	11	Cap Sense 13	18	Cap Sense 6	25	GND
5	Cap Sense 19	12	Cap Sense 12	19	Cap Sense 5		
6	Cap Sense 18	13	Cap Sense 11	20	Cap Sense 4		
7	Cap Sense 17	14	Cap Sense 10	21	Cap Sense 3		

JF1							
PIN	Description	PIN	Description	PIN	Description	PIN	Description
1	GND	12	Cap Drive 9	23	Cap Drive 20	34	Cap Drive 31
2	GND	13	Cap Drive 10	24	Cap Drive 21	35	Cap Drive 32
3	Cap Drive 0	14	Cap Drive 11	25	Cap Drive 22	36	Cap Drive 33
4	Cap Drive 1	15	Cap Drive 12	26	Cap Drive 23	37	Cap Drive 34
5	Cap Drive 2	16	Cap Drive 13	27	Cap Drive 24	38	Cap Drive 35
6	Cap Drive 3	17	Cap Drive 14	28	Cap Drive 25	39	Cap Drive 36
7	Cap Drive 4	18	Cap Drive 15	29	Cap Drive 26	40	Cap Drive 37
8	Cap Drive 5	19	Cap Drive 16	30	Cap Drive 27	41	Cap Drive 38
9	Cap Drive 6	20	Cap Drive 17	31	Cap Drive 28	42	Cap Drive 39
10	Cap Drive 7	21	Cap Drive 18	32	Cap Drive 29	43	GND
11	Cap Drive 8	22	Cap Drive 19	33	Cap Drive 30		

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# 3.3 Interface pin definition

PM2304 series includes USB / I<sup>2</sup>C /UART communication interfaces, intends to maximize application flexibility and reliability, and minimizes cost through elimination of external components.

JL1 / 5PIN / UYT 1W1258WOR0-05K									
PIN NO.	USB	Description	Min	Тур	Max	Unit			
1	VCC	Positive power supply		5		V			
2	D-	D- pin of internal USB transceiver		3.3		V			
3	D+	D+ pin of internal USB transceiver		3.3		V			
4	GND	Ground		0		V			
5	SW	Pull low for disable touch function, release this				V			
		pin will back to enable touch function, Leave the							
		pin unconnected if not used.							

# USB

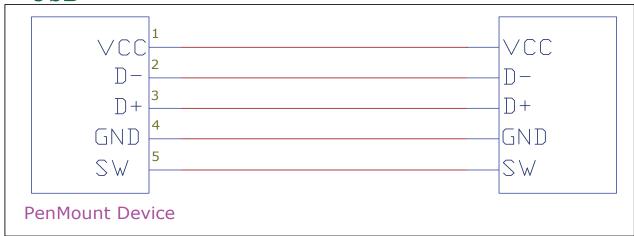
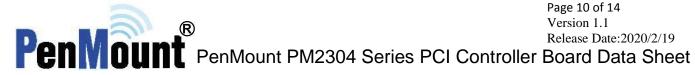


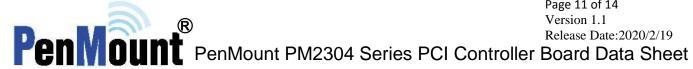
Figure 3 USB interface



JL2 / 7PIN ; ACES 50224-00701-001							
PIN NO.	CVAADOL	PIN ASSIGNMENT					
PIN NO.	SYMBOL	I <sup>2</sup> C	UART				
1	VCC	VCC	VCC				
2	GND	Ground	Ground				
3	SCL / RXD	SCL	RXD				
4	SDA / TXD	SDA	TXD				
5	nRESET	N.C.	N.C.				
6	nDETECT	N.C.	Low				
7	nINT	nINT	Low				

PIN NO.	Туре	Description	Min	Тур	Max	Unit
VCC	Р	Positive power supply		5		V
GND	Р	Ground		0		V
SCL	I/O	Serial clock line for I <sup>2</sup> C. Open drain requires		3.3		V
		external pull-up to 3.3V.				
SDA	I/O	Serial data line for I <sup>2</sup> C. Open drain requires		3.3		V
		external pull-up to 3.3V				
RXD	I	UART receive		3.3		V
TXD	0	UART transmit		3.3		V
nRESET	I	Open-drain and active low to reset PM2304				V
		and must be driven low for 5 µs (typical) to be				
		valid. Leave the pin unconnected if not used.				
nDETECT	I	Pull low when selecting UART interface		0		V
		float when selecting I <sup>2</sup> C interface				
nINT	0	Processor Interrupt. This pin is active low,		3.3		V
		open drain requires external pull-up to 3.3V.				

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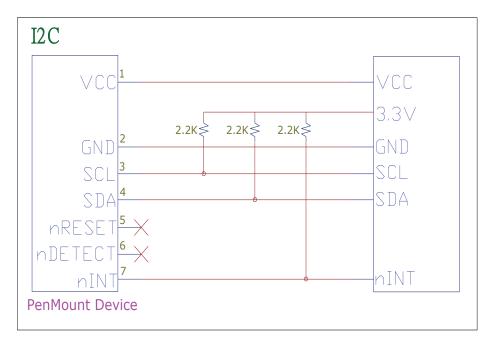


Figure4 I<sup>2</sup>C interface

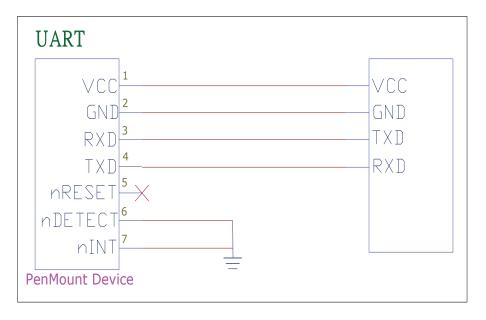
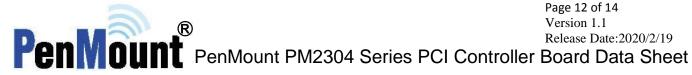
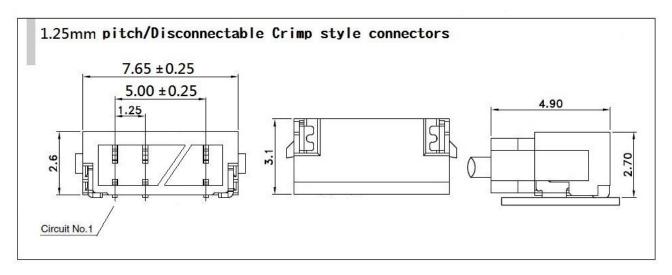


Figure 5 UART interface

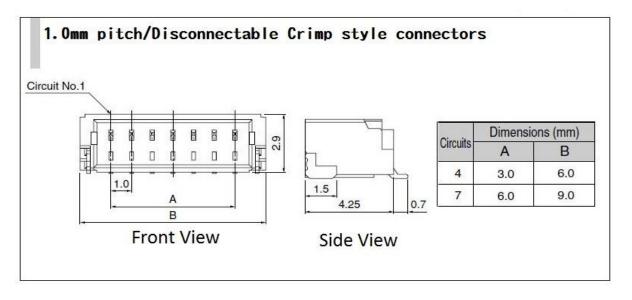


# 3.4 Connector specifications

# JL1 USB Connector



# JL2 I2C/UART Connector



Website: <a href="http://www.penmount.com">http://www.penmount.com</a> E-mail: penmount@seed.net.tw

# Pen Nount PM2304 Series PCI Controller Board Data Sheet

### **Drivers and Utilities** 4.0

# 4.1 Drivers

# For I<sup>2</sup>C:

- Windows CE: Binary driver for freescale iMX platform. Other platform by request.
- Linux / Android : Source code for integration.

# For USB

- Windows 2000, XP, 2003: single touch, mouse driver.
- Windows Vista: single touch, inbox driver.
- Windows 7,8,10: multi touch, Inbox driver.
- Linux: Ubuntu, Android, other Linux distributions under development.

# For UART

- Windows 2000, XP, 2003: single touch, mouse driver.
- Windows Vista: single touch, digitizer driver.
- Windows 7,8,10: 5 touches support, digitizer driver.
- Linux: inbox driver after kernel 3.2, provide source code for kernel 2.6

(Provide source code for integration if any)

## 4.2 Utilities

Firmware adjustment utility allows user to fine tune the touch panel sensitivity.

# Note:

All drivers and utilities are available on PenMount websites. Please contact us for further information.



# Pen Vount PM2304 Series PCI Controller Board Data Sheet

# 5.0 Others

# 5.1 ROHS compliance

This control board is ROHS compliant

# 5.2 EMC protection recommendations

Please refer to PCI touch screen integration guides.

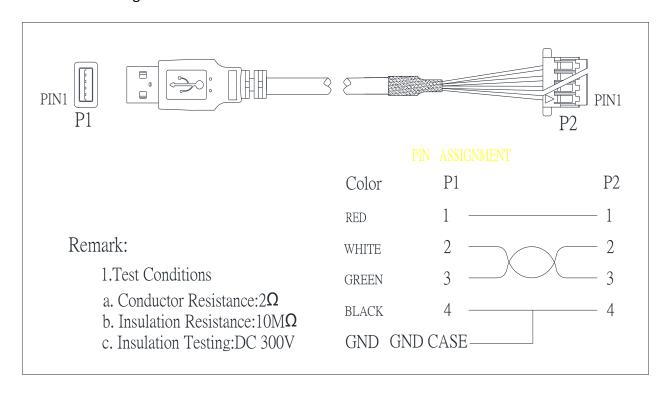
# 5.3 Noise Protection

To achieve good noise interference protection capabilities, PenMount requires paired interface cables possess comprehensive EMI shielding.

The cable should have a woven or spirally copper shield with 360 ° shield coverage

The shield must be terminated to the receptacle and be connected to ground plane carefully.

Below is an example for 4-pin USB cable diagram. For other implementation, please follow the same design rules.



Website: <a href="http://www.penmount.com">http://www.penmount.com</a>
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